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Confirmation No. 1990
Mail Stop Issue Fee

Re: Allowed U.S. Utility Patent Application
Appl. No. 10/780,877; Filed: February 19, 2004
For: **Overlay Correction By Reducing Wafer Slipping After Alignment**
Inventor: Peter KOCHERSPERGER
Our Ref: 1857.2140000

Sir:

In response to the **Notice of Allowance and Fee(s) Due** dated June 24, 2010, the following documents are transmitted for appropriate action by the U.S. Patent and Trademark Office:

1. Issue Fee Transmittal (Form PTOL-85);
2. Comments on Statement of Reasons for Allowance; and
3. Online Credit Card Payment Authorization for \$1,810.00 to cover:
\$1,510.00 - Issue Fee; and
\$300.00 - Publication Fee.

The above-listed documents are filed electronically through EFS-Web.

Fee payment is provided through online credit card payment. The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

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JDE/lvt
Enclosures